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A geometric approach for PV modules degradation

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Abstract—This paper presents an overview of two degradation modes of PV panels: the Moisture Induced Degradation and the Cell Cracks. The two mentioned degradation modes affect PV cells differently according to their position in the module. At a previous stage, we have build a PV model that takes into consideration three degradation modes, the Potential Induced Degradation, the Light Induced Degradation, and the Ultraviolet Light Degradation. In this paper, we update our model to take into consideration all degradation modes.

Index Terms—Moisture Induced Degradation, Cracks Degradation, Geometric position, Efficiency, Modeling.

I. INTRODUCTION

With the growth of PV (Photovoltaic) part in energy production, reliability and degradation are two concerns of main importance that must be studied for module life span. In fact, investors in power production are expecting the minimum price per kWh produced. From here derives our objective to increase the life span of PV panels.

Other arguments defend our objective. In fact, PV panels need at least three years to generate the power needed for their fabrication. Second, the fabrication of PV panels needs rare raw materials like Indium, Silver, Germanium, and Gallium. Third, some PV panels contain carcinogenic elements like Cadmium and Lead [1].

Many PV specialists are studying the degradation of PV panels [2-8]. They use accelerated tests to show the effect of environmental conditions on PV panels. Melissa A. Yaklin and al. showed the effect of humidity on TCO (Transparent Conductive Oxide) [2]. Michael D. Kempe focused on the ingress of moisture into PV panels [3]. Jane Kapur and al. showed the geometrical repartition of moisture in the PV panel [4]. On the other hand, Sarah Kajari-Schröder and al. showed the geometrical and directional repartition of cracks in a PV panel [5]. S. Kajari-Schröder and al. explained when cracks become critical to PV cell [6]. M. Köntges and al. simulated the effect of cracks on power production [7].

In previous works, we developed a PV model that simulates the efficiency as a function of time. Our model takes into consideration the PID (Potential Induced Degradation), the LID (Light Induced degradation), and the UVD (Ultraviolet Light Degradation) [8]. Our panel is made of two strings of 12 cells each. Each cell delivers 5.5A under the STC (Standard Test Conditions).

In this paper, we represent first the equivalent circuit of a PV cell. Then we overview the MID (Moisture Induced Degradation), and we model the effect of moisture on the equivalent circuit of PV cells. The modeling takes into consideration the position of cells in the panel. Then we overview the cell cracks. And we model their effect on the equivalent circuit of PV cells. At the end, we upgrade our existing model to consider all degradation modes. We use our model to evaluate the efficiency of PV panels as a function of time and environmental conditions.

Mismatch losses that arise from un-uniform degradation are underlined in this article.

II. EQUIVALENT CIRCUIT OF A PV CELL

A PV cell can be modeled like a current source; its electrical model is compose

d of a current source (I_{SC}) mounted in parallel with two diodes (D1 and D2) and a shunt resistor (R_{sh}). A series resistor (R_s) is added to complete the model. The current generated I_{SC} is proportional to the incident irradiance G in $W.m^{-2}$ and to the temperature T in K. The current flowing in the shunt resistor (R_{sh}) represents the leakage current in the edge of the cell. The resistor R_s represents the internal losses of the PV cell caused by the electric contact. The current I (in A) flowing in the load will become [1]:

$$I = I_{SC} - I_{01} \left(\exp\left(\frac{e(V+IR_s)}{KT}\right) - 1 \right) - I_{02} \left(\exp\left(\frac{e(V+IR_s)}{2KT}\right) - 1 \right) - \frac{(V+IR_s)}{R_{sh}} \quad (1)$$

Where, I_{01} : is the saturation current of diode D1 in A.
 I_{02} : is the saturation current of the diode D2 in A.
 e : is the electron charge ($1.6 \times 10^{-19} C$).
 V : is the output voltage of the cell in V.
 K : is the Boltzmann constant $1.3806 \times 10^{-23} J.K^{-1}$.
 T : is the temperature in K.

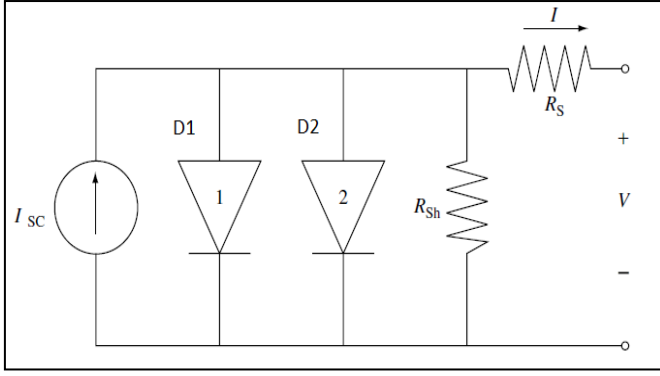


Figure 1. Equivalent circuit of a PV cell.

III. GEOMETRY OF A PV PANEL

The current and the voltage delivered by one solar cell are small for being useful. This implies the need of connecting cells in series and in parallel to form a module capable of delivering useful voltage and current. In this paper, we consider a panel composed of two strings of 12 cells each. We adopt the configuration shown in Fig 2.

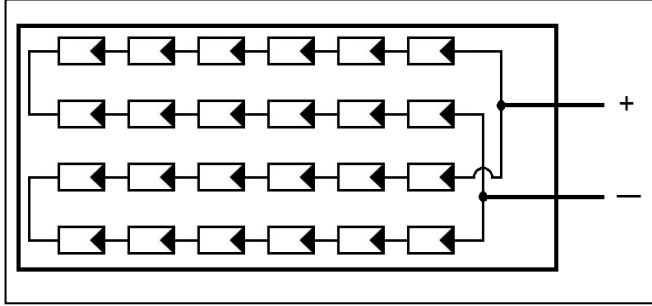


Figure 2. Desired configuration of the panel model.

IV. MID: MOISTURE INDUCED DEGRADATION

A. MID overview

PV panels are subject to moisture degradation. This is why they undergo a **Damp Heat test (DH)**. In the DH test, the panels or cells are exposed to an environment specified by an 85% RH (**Relative Humidity**) and an 85 °C. The test lasts for 1000h. According to IEC61646 standard, the efficiency of the panels must not decrease more than 5% [9]. Humidity degrades PV panels by causing delamination and mainly increasing the series resistance of the equivalent circuit. In fact, it degrades the TCO layer.

B. MID Modeling

The TCO layer is used in thin film technologies because of its conductivity, transparency, and low cost. The TCO degrades under effect of humidity; its resistance increases. To investigate the effect of humidity on the resistance of the TCO, many experiments in literature were elaborated [2-3-4,9]. Results of these experiments done by Melissa A. Yaklin and al. show that degradation follows an Arrhenius equation with an

activation energy of 0.91 eV [2]. Besides, temperatures lower than 44°C did not show any degradation. In addition, TCO degradation is proportional to the relative humidity. Analyzing the literature experiences and using an empiric method we can derive the resistance of the TCO layer as a function of time by:

$$R_s = R_0 + 4 \times 10^{10} \times \exp\left(\frac{-87500}{R \times T}\right) \times RH \times t \quad (2)$$

Where, R_0 : initial TCO layer resistance in Ω .

R: Gas constant 8.314 J.mol⁻¹.K⁻¹.

T: temperature in K.

RH: relative humidity.

t: time in hours.

It is clear that high degradation occurs when high temperature and high relative humidity combines [2].

As humidity affects TCO layer of PV panels, we must study the ingress of humidity into the panel. We define the **WVTR (Water Vapor Transmission Rate)** which indicates the passage of water trough the encapsulant layer. The WVTR in g.cm⁻².s⁻¹ is given by the following formula [4]:

$$WVTR = \frac{P}{l} \left[1 + 2 \sum_{n=1}^{\infty} (-1)^n \exp\left(\frac{-Dn^2 \pi^2 t}{l}\right) \right] \quad (3)$$

Where, P: permeability in cm².

l: encapsulant thickness in cm.

D: diffusion coefficient in cm².s⁻¹.

t: time in s.

At steady state, the WVTR is equal to $\frac{P}{l}$. We must note

that the WVTR follows an Arrhenius equation with an activation energy of 0.38 eV. We must also note that moisture is susceptible to ingress through the edges of the panel. Mathematically, with some approximation, moisture repartition follows a sine function having the lower value at the center of the panel and the higher value near the edges [4].

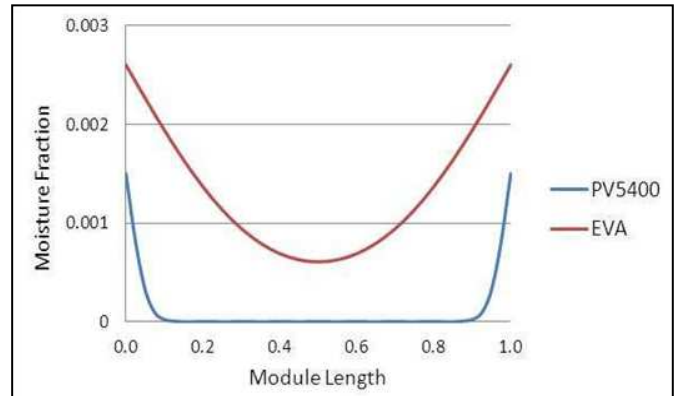


Figure 3 20 years from [4]

The RH in equation 2 can be substituted by the actual level of moisture fraction from Fig 3. We consider PV modules

having an **EVA (Ethylene Vinyl Acetate)** encapsulation and a TCO layer. The series resistance of each cell is given by:

$$R_s = 0.1 + 4 \times 10^{10} \exp\left(\frac{-87500}{R \times T}\right) \sin(Dist) \exp\left(\frac{-36678}{R \times T_{av}}\right) RH_{ext} t \quad (4)$$

Where, Dist: Distance from centre (Table 1).
 RH_{ext} : external Relative Humidity.
 T: temperature of the Panel in K.
 T_{av} : average temperature in K.
 t: time in hours.

Table 1. Normalized distance from centre.

1	0.75	0.75	0.75	0.75	1
0.75	0.5	0.25	0.25	0.5	0.75
0.75	0.5	0.25	0.25	0.5	0.75
1	0.75	0.75	0.75	0.75	1

We consider that the ingress of moisture is a relative low phenomenon; the temperature used to calculate the ingress is the average temperature. The degradation happens at the actual temperature of the panel.

V. CELL CRACKS

A. Cells Cracks overview

PV panels are laminates generally formed by pilling a glass superstrate, EVA encapsulation, cells, another EVA encapsulation, and the backsheet. The panel is then sealed with an aluminum frame. During manufacturing process and when exposed to mechanical loads (transportation, wind, snow...), cells suffer from cracks formation due to their plastic properties.

We focus on crack formation in crystalline silicon based PV cells. The cells are connected using a silver printed grid on the surface of the cell. A copper bus-bar is soldered perpendicularly to the silver grid to connect adjacent cells.

After formation a crack may not be harmful to a PV cell. We talk about cracks criticality. In fact, cracks divide the cell area into regions. A region becomes inactive if it is no more connected to the bus-bar. From the latter, if a crack is perpendicular to the bus-bars, it will not affect the cell. If a crack is parallel to the bus-bars and is situated between the two bus-bars, it will not affect the cell. If a crack is parallel to the bus-bars and is situated in the external area, it will isolate an area of the cell making it inactive Fig 5 [5]. Dendritic cracks may also lead to some isolated areas of the cell.

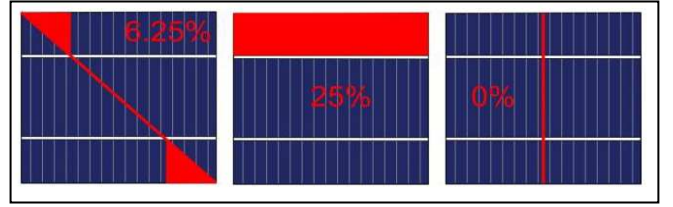


Figure 4. Cracks orientation from [5].

A PV panel is mechanically modeled as a Navier plate; the frame is defined to have infinite stiffness and the laminate is considered to have the bending of a glass [5]. One of the most important environment effects on cell cracks is the snow. The latter is modeled by a uniform load on the Navier plate. Simulations and examinations of panels done by Schröder and al. give the following repartition and direction of cracks:

- Cracks are likely to happen on the diagonals and more on the center
- Cracks formation is negligible near the middle of edges
- Cracks orientation is parallel to diagonals in the diagonals
- Cracks orientation is parallel to the long edges in the centre

It is clear that the most deteriorated cells are those located at the center and at the diagonals. In the center, strain is parallel to the small edges of the panel forming cracks parallel to the large edges of the panel or parallel to the bus-bars. In the diagonals, strain is perpendicular to the diagonals forming cracks parallel to the diagonals or making an angle with bus-bars [5].

Although orientation is important, it does not affect cells operation until it becomes deep and isolates cell areas. Based on experimental results, many cycles of humidity-freeze are needed to deepen the cracks and decrease the power output of the panel [6].

B. Cells Cracks Modeling

Cracks are generally a recombination center for electron-hole pairs. However, when cell regions are isolated or inactive, the photo-generated current $I_{SCcracked}$ and the recombination current ($I_{01cracked}$ and $I_{02cracked}$) will decrease respecting the following equations [7, 10]:

$$I_{SCcracked} = I_{SC} \left(1 - \frac{A_{inactive}}{A_{total}} \right) \quad (5)$$

$$I_{01cracked} = I_{01} \left(1 - \frac{A_{inactive}}{A_{total}} \right) \quad (6)$$

VI. SIMULATION

$$I_{02cracked} = I_{02} \left(1 - \frac{A_{inactive}}{A_{total}} \right) \quad (7)$$

Where, $I_{SCcracked}$: the short circuit current after crack formation in A.

$A_{inactive}$: isolated area of the cell in m^2 .

A_{total} : total area of the cell in m^2 .

$I_{01cracked}$: saturation current of D1 of the cracked cell in A.

$I_{02cracked}$: saturation current of D2 of the cracked cell in A.

As PV panels generally expected to operate at MPP, the I_{MPP} (current at maximum power point in A) is lower than the I_{SC} , no power decrease is noted if the following equation is respected [7]:

$$\frac{A_{inactive}}{A_{total}} < \frac{I_{SC} - I_{MPP}}{I_{SC}} \quad (8)$$

However, when the isolated cell area increases (exceeding 8%), we start noticing power losses. The cell becomes reverse biased and the current of the string flows through it. If its breakdown voltage is high, the current flows in the bypass diode if present [7]. It must be noted that having one or more cracked cells in a string lead approximately to the same power loss. The power loss depends from the cell having the maximum isolated area.

Table 2 is used to assign the fraction of inactive area of each cell according to the overview discussed in section V.

Table 2. Inactive area repartition.

0.4	0	0	0	0	0.4
0	0.4	0.6	0.6	0.4	0
0	0.4	0.6	0.6	0.4	0
0.4	0	0	0	0	0.4

A. Simulation of a TCO based Panel

Panels equipped with a TCO are not crystalline based panels, they are CdTe or dye-sensitized panels. They undergo PID, UVD, and MID degradation.

The PV panel Model is built under Matlab/Simulink/Simscape. It consists of two strings of 12 cells each. They are configured according to Fig 2. The series resistor R_s of the equivalent circuit is varied according to equation 4 and to table 1. The shunt resistor R_{sh} and the series resistor R_s vary also because of PID and UVD degradations [8]. The temperature is made variable during the day and during the year. The relative humidity is 50%, the panel operates at 80V to ground, and the irradiance is 1000 W/m^2 . The power output at STC is measured. The Normalized efficiency is calculated for a period of 10 years following the equation:

$$NormalizedEfficiency(t) = \frac{P_{STC}(t)}{P_{STC}(0)} \quad (9)$$

In Fig 5, we see the simulation result of the TCO based panel. The normalized efficiency is represented for three different temperatures; 55°C in blue, 45°C in red, and 35°C in magenta. It is clear that high temperature increases the degradation of PV panels. First we have a high degradation caused by PID until the leakage current saturates [8]. The other degradations remains.

B. Simulation of a grid based Panel

Panels equipped with a metallic grid are generally crystalline based. They undergo PID, LID, UVD degradations and may be subject to cracks.

The PV panel Model is built under Matlab/Simulink/Simscape. It consists of two strings of 12 cells each. They are configured according to Fig 2. The short circuit current I_{sc} and the diodes saturation currents I_{01} and I_{02}

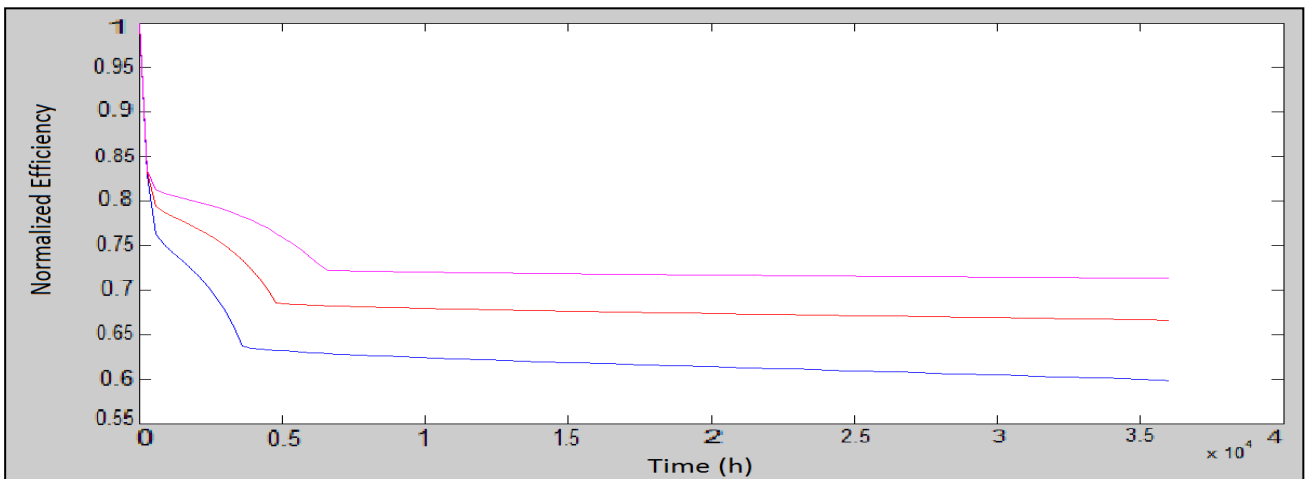


Figure 5. Normalized efficiency of a TCO based panel for 10 years.

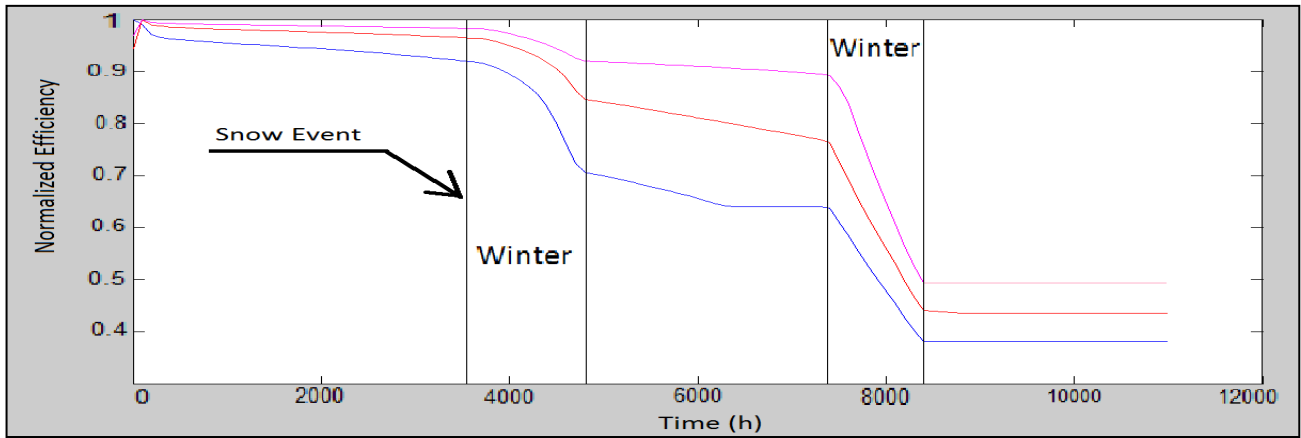


Figure 6 Normalised efficiency of a grid based PV panel for 3 years.

are varied according to equations 5, 6, and 7. The shunt resistor R_{sh} , the series resistor R_s , and the diodes saturation currents I_{01} and I_{02} also vary because of PID, LID, and UVD degradations [8]. The relative humidity is 50%, the panel operates at 80V to ground, and the irradiance is 1000W/m^2 .

We consider one snow event that produces the cracks. Then we will need 200 nights (two winters) of freeze to reach the maximum degradation point. We will adopt a linear degradation during the 200 freeze nights.

The power output at STC is measured. The Normalized efficiency is calculated for a period of 3 years following equation 9.

In Fig 6, we see the simulation result for grid based PV panel. The normalized efficiency is represented for three different day temperatures; 15°C in blue, 25°C in red, and 35°C in magenta. It is clear that high temperature increases the degradation of PV panels. The snow cracking event is programmed to happen at the beginning of the second operating year ($t=3650\text{hours}$). The effect of cracks increases during winter.

VII. CONCLUSION

In this paper we presented two degradation modes of PV panels. The MID that increases the cell TCO resistance was described and simulated. Then the cells cracks were described and simulated for crystalline silicon panels. The degradations modes found in literature were analyzed and studied to build a mathematical model of a PV panel that degrades with time. The two models take also into consideration the PID, the LID and the UVD. The simulation results show the efficiency decrease of PV panels as a function of environmental situation.

To mitigate MID degradation process we must build good sealed panels to avoid ingress of moisture. Another way is to decrease the temperature of the panel.

To mitigate the cracks effects on PV panels we must first try avoiding cracks formation or prevent freeze cycles. We may also design a metallic grid that is not affected by the cell cracks.

Our objective is to increase PV panel's lifetime to 40 years. Our future work will focus on exploring all degradation modes. Then we will try to apply ICT (Information and Communication Technologies) to alleviate the effect of environment on degradation of PV panels.

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